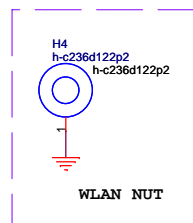
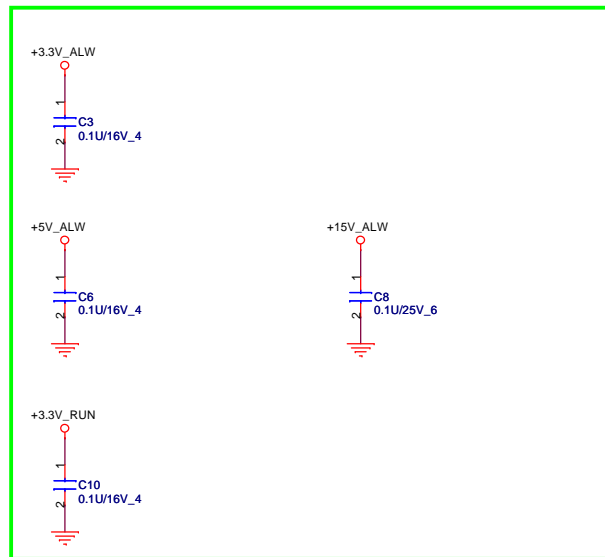

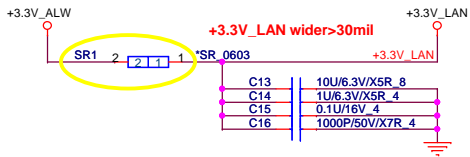


Change CN1 footprint from "88079-0800a1-80p-ldh" to "88079-0800a1-80p-ldh-smt"



 Quanta Computer Inc. PROJECT : R08	
Size	Document Number
BTB Conn.	
Date: Monday, February 13, 2012	Sheet 1 of 4

These Caps close to LAN Chip VDD33 pins

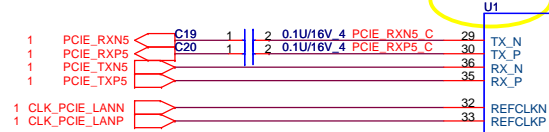


20120204
Change R1 to SR1(SJ0603)

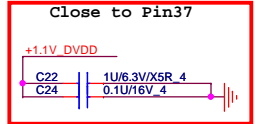
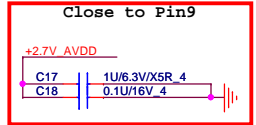
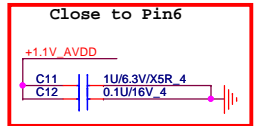
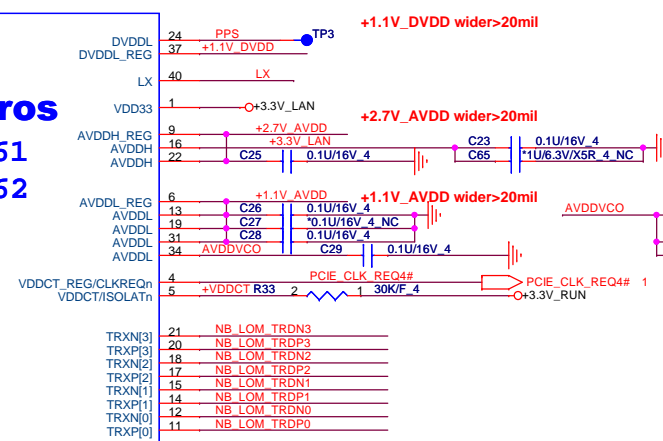
	PN	Footprint	SKU
AR8161	AL008161002	qfn40-5x5-4-41p-0_9h	R08T, V08, V08A
AR8162	AL008162000	qfn40-5x5-4-41p-0_9h	R08, R08A

20120203

Change U1 PN to AL008162001(ver B)

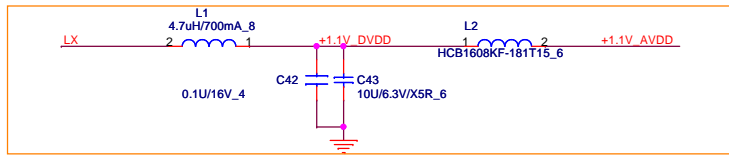
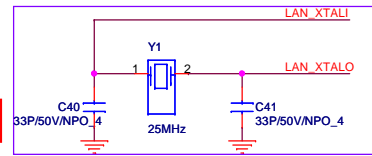


Atheros
AR8161
AR8162



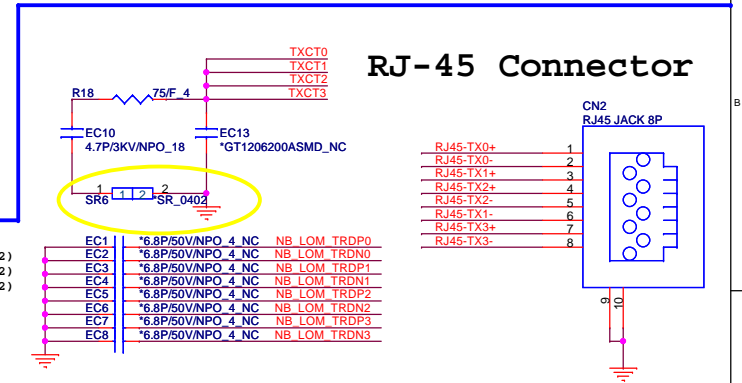
Crystal BOM	
Crystal BOM	POP C40 C41 Y1, NC R37 EC12
PCH clock BOM	NC C40 C41 Y1, POP R37 EC12

LED1 (Pin39)	Regulator select		L1	L2	C42	C43
	LDO	Low	NC	NC	NC	NC
	SWR*	High	V	V	V	V



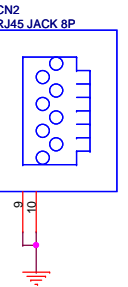
LED_ACT	Over-clocking enable	Over-clocking disable
	1	0

LED1	LDO mode(Default)	SWR mode
	Mount R35	NC R35



RJ-45 Connector

RJ45-TX0+	
RJ45-TX0-	1
RJ45-TX1+	2
RJ45-TX2+	3
RJ45-TX3+	4
RJ45-TX0-	5
RJ45-TX1-	6
RJ45-TX2-	7
RJ45-TX3-	8



LAN Co-layer information

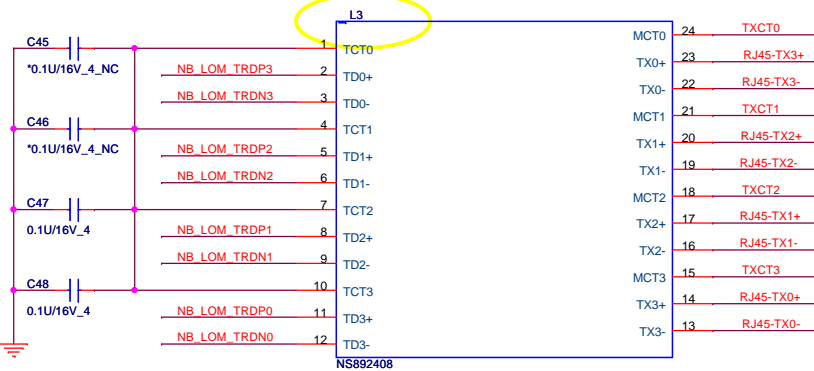
	C45	C46	C47	C48	C27
	AR8161	V	V	V	V
AR8162	NC	NC	V	V	NC

20120203

Change L3 to DB0EF7LAN01(FCE)

20120206

Change R58 to SR6(short0402)
Change R14 to SR7(short0402)
Change R19 to SR8(short0402)



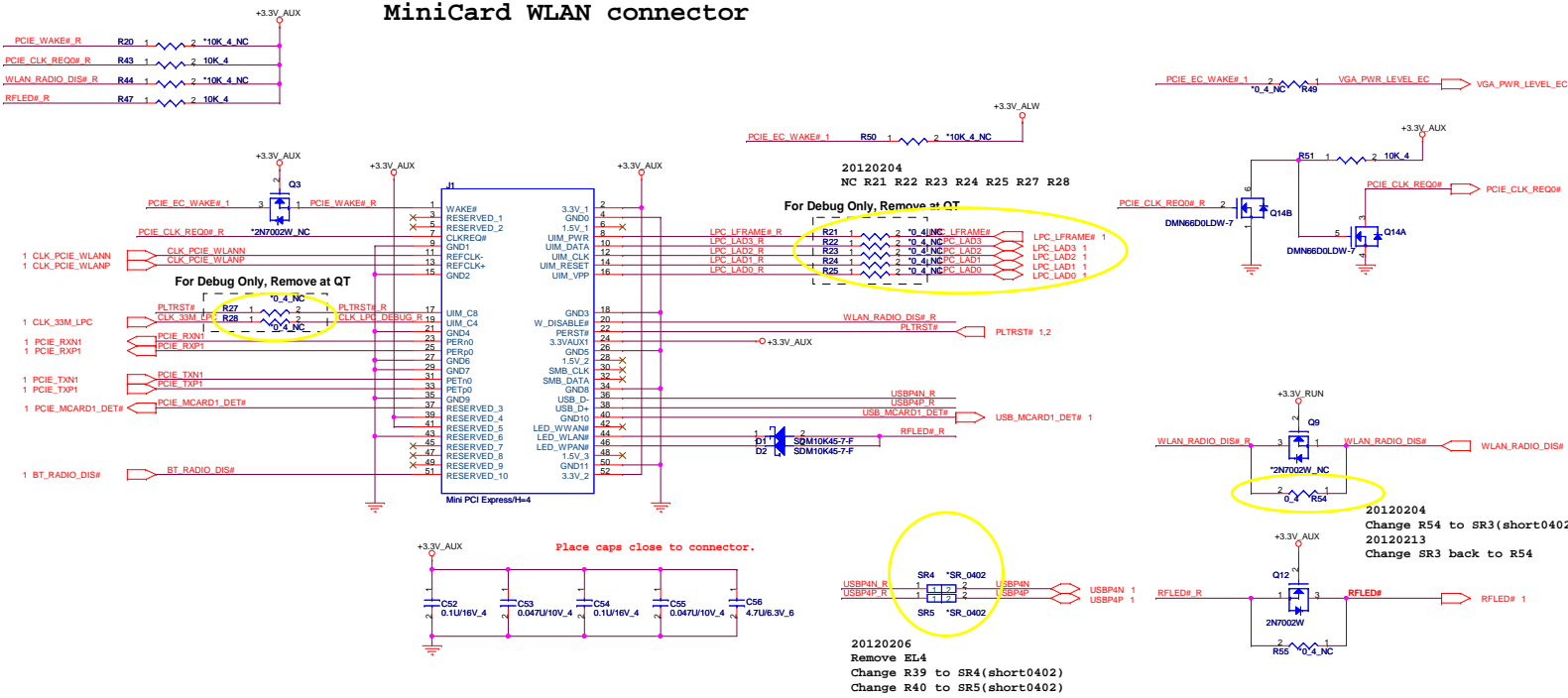
Transformer Co-layer information (follow R01)

Mode	L3(transformer)	U1
10/100	NS892408 (DB0EF7LAN01)	AR8162-A
Giga	LF89276C-R (DB0LL1LAN00)	AR8161-A



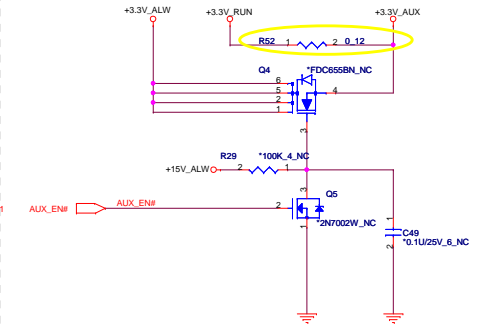
Quanta Computer Inc.
PROJECT : R08

MiniCard WLAN connector

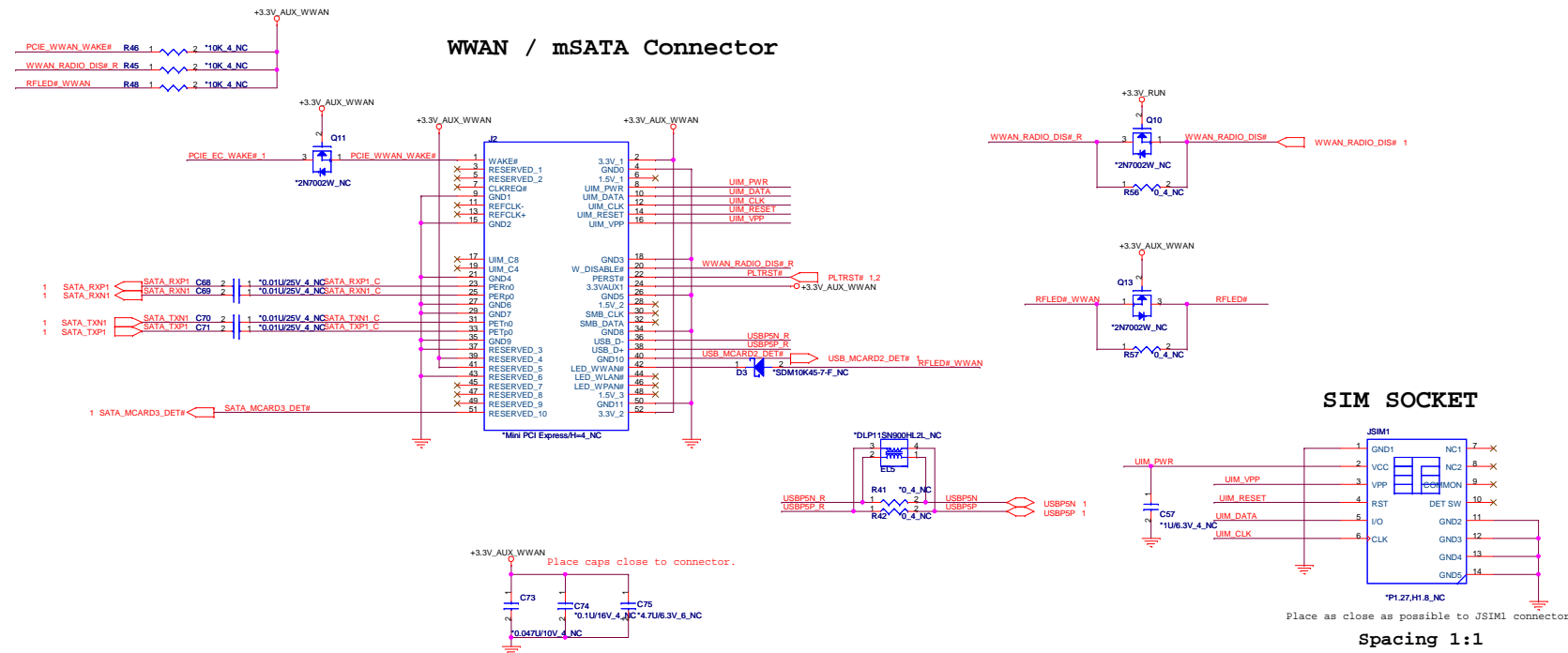


Support AOAC on WLAN

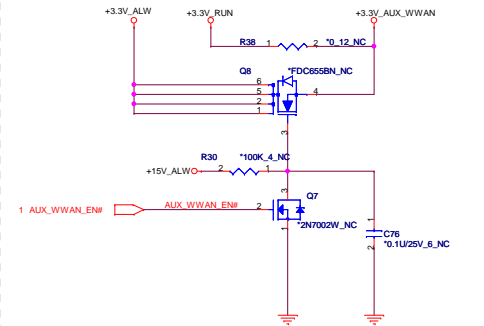
20120204
Change R52 to SR2(short1206)
20120213
Change SR2 back to R52



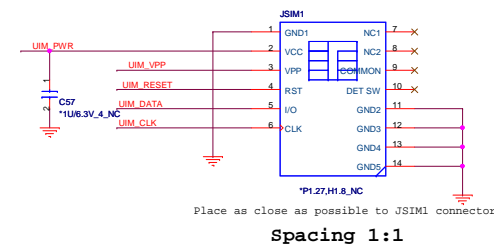
WWAN / mSATA Connector

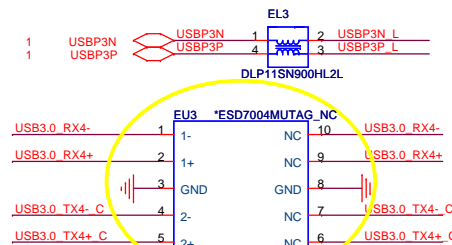
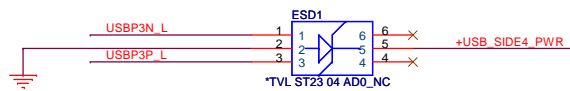
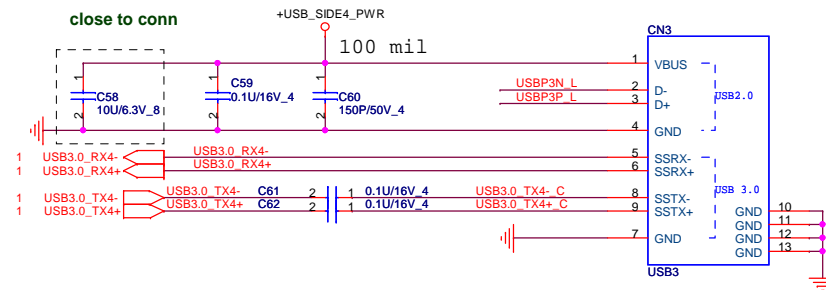
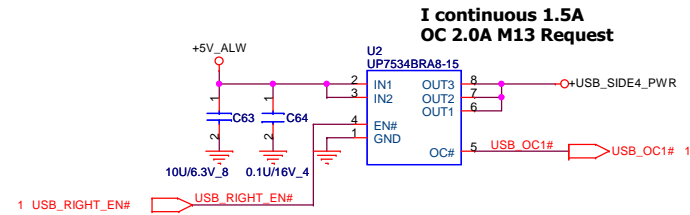


Support AOAC on WWAN



SIM SOCKET





ESD Function

20120213
NC EU3